

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: <b>Brent A. Anderson et al.</b>	Date: <b>11/07/2007</b>
Serial Number: <b>10/596,029</b>	Examiner: <b>Thao X. Le</b>
Filed: <b>05/25/2006</b>	Group Art Unit: <b>2814</b>
Title: <b>Semiconductor memory device with increased node capacitance</b>	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

#### AMENDMENT

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated August 15, 2007, the following Amendment is being submitted to place the above-identified application in *prima facie* condition for allowance.

Kindly amend the following claims and consider the following Remarks: